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Cadence Design Systems

● 회사 소개

EDA(Electronic Design Automation) 분야의 세계 최대 마켓리더인 케이던스(Cadence Design Systems Inc.)는 전세계 반도체, 통신장비, 멀티미디어 및 가전제품 회사들이 모바일, 컨슈머, 클라우드, 데이터 센터, 자동차, 항공우주 산업, IoT 등 다양한 분야에서 다양한 제품을 적기에 개발, 생산 할 수 있도록 EDA 소프트웨어와 하드웨어, IP 및 디자인 서비스를 제공하고 있습니다. 세계 유수한 기업들은 물론 국내에서도 여러 주요 기업에서 전자설계를 위한 표준 툴로 사용하고 있습니다.
미국 캘리포니아주 산호세에 본사를 두고 세계 각지에 현지법인, 연구소, 디자인 센터를 두고 있으며, 포춘 100대 기업 리스트에 이름을 올렸습니다.
- Learn more at www.cadence.com

FORTUNE 100 BEST COMPANIES TO WORK FOR 2020

● 보유기술

1. EDA 전반 기술
2. AI/ML
3. System Analysis 기술

● 주력제품 및 서비스

1. Digital Design Tools: Genus - Modus - Tempus - Innovus
2. Analog Design Tools: Virtuoso - MMSIM/Spectre - Quantus - Pegasus - CMP/LPA
3. Verification Tools: Xcelium / P-Z1 / P-X1
4. Analysis Tools: PCB / Package(SIP) / Voltus / Sigrity / Clarity / Celsius / AWR / EMX

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Cadence Business Groups and Products

Digital and Signoff

Front-End Design

- **Stratus™** : High-level synthesis
- **Genus™** : RTL synthesis
- **Modus** : Test
- **Joules™** : RTL power analysis
- **Conformal®**: Formal equivalence

IC Digital

- **Innovus™** : Implementation

Silicon Signoff and Verification

- **Quantus™** : Extraction
- **Liberate™** : Library characterization
- **Tempus™** : Timing (STA)
- **Pegasus™** : Physical signoff
- **DFM / OPC** : Design for manufacture / optical proximity correction

Custom IC and PCB

Custom IC

- **Virtuoso®**: Custom/RF/mixed-signal and system design
- **Spectre®** : Simulation
- **Legato™** : Reliability analysis
- **AXIEM®** : General RF/microwave EM analysis
- **Microwave Office®** : Microwave RF design

IC, Package, and Board

- **Allegro®** : IC package and PCB design
- **OrbitIO™** : System-design planning
- **OrCAD®** : Mainstream PCB design

Multi-Physics System Analysis

- **Clarity™** : Electromagnetic analysis
- **Celsius™** : Electro-thermal analysis
- **Sigrity™** : System SI, PI, and thermal analysis
- **Voltus™** : Power integrity analysis
- **EMX®** : Specialized RFIC parasitic analysis

System Verification

Hardware System Verification

- **Palladium®** : Emulation
- **Protium™** : FPGA prototyping
- **SpeedBridge®** : Physical adapters
- **VirtualBridge™**: Virtual adapters

Advanced Verification Solutions

- **Xcelium™ ML** : Xcelium logic simulator with Machine learning
- **Xcelium™** : IP and SoC simulation

Verification Fabric and Formal Solutions

- **JasperGold®** : Formal verification
- **System VIP** : System Verification IP
- **VIP** : Interface Verification IP
- **Perspec™** : Portable stimulus
- **vManager™** : Verification management
- **Indago™** : IP and SoC debug
- **Functional Safety**

Intellectual Property

Tensilica® IP

- **DNA 100 DSPs** : Edge AI inferencing
- **Vision DSPs** : Imaging, neural networks
- **ConnX DSPs** : Baseband, communications
- **HiFi DSPs** : Audio, voice, speech
- **Fusion DSPs** : IoT and general purpose

Design IP

- **Denali® Memory IP** :DRAM and flash interfaces
- **Interface IP** : Serial interfaces
- **Analog IP** : Analog/digital converters

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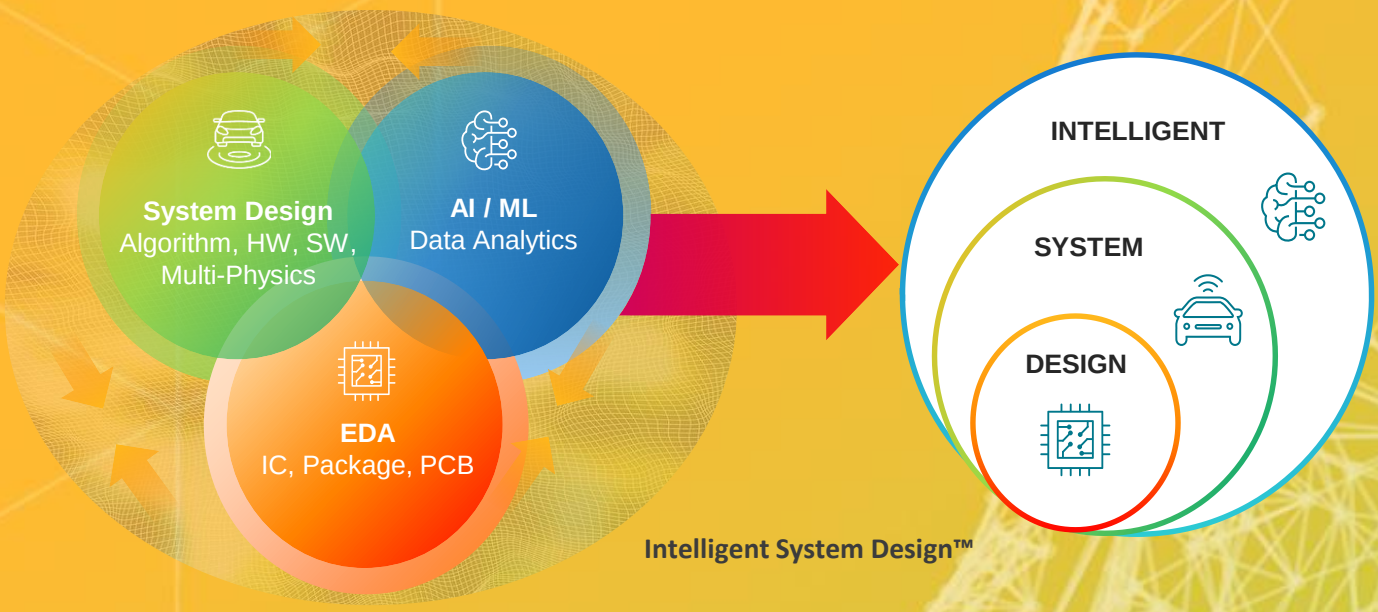
Cadence Is Leading the Computational Software Convergence



Intelligent System Design™

Merger of EDA, system design, AI | Pervasive intelligence throughout design | Grounded in computational engineering

Co-optimizing system, hardware, software | Spanning multiple system domains

AI / Machine Learning Solutions

- Design excellence and enablement

Key Benefits

Artificial intelligence (AI) promises to revolutionize people's lives. Whether it's autonomous cars or advances in the medical industry, we will all benefit from this revolution. Convolution and recurrent neural networks and machine and deep learning algorithms present the opportunity to enable this electronics revolution and create a new silicon renaissance with advances in software and IP.